About SignOff Semiconductors

SignOff Semiconductors is a consulting company that was founded in 2015 by a group of semiconductor professionals. Since then, the company has provided design services to several companies in the semiconductor industry through continuous service partnerships. SignOff Semiconductors is a fast-growing company with a deep focus on getting excellent talent from the industry as well as picking exceptional talent from the academics.

Our unique and transparent work culture has helped us to retain the best talent and we collectively deliver high quality design services. Our team has a vast experience, and we can serve our clients on various services like Physical Design, Full Custom Analog and Digital Custom Layout and Verification, RTL Design, Verification Embedded and Firmware.

SignOff Semiconductor has offices in Bengaluru, Hyderabad, Toronto (Ontario, Canada) and California (US) in order to serve its customer based on their asks and needs.

Kaveri
Risc Based ASIC
Robust, Low power, Customizable, Scalabel

Since 2015
Operational
From day one our focus is to deliver Excellence Everyday

350+ Years
Cumulative Team Experience
From day one our focus is to deliver Excellence Everyday

Simple, Robust, Optimized, Dashboard, Automation
In House ASIC / SoC Flow
Anvay

30+ Partnerships
Partnership
Partnership with leading semiconductor companies, EDA, IP and Foundry services to deliver differentiated custom ASICs and IPs

Graphics, IoT, Automobile, N/W ICs, CPU, Modem, Custom - ASIC, SoCs
Tape Outs
200+

Contact Us

📞 +91 80 2990 2871

✉ hr@signoffsemi.com

✉ sales@signoffsemi.com

🌐 www.signoffsemiconductors.com

Our Locations

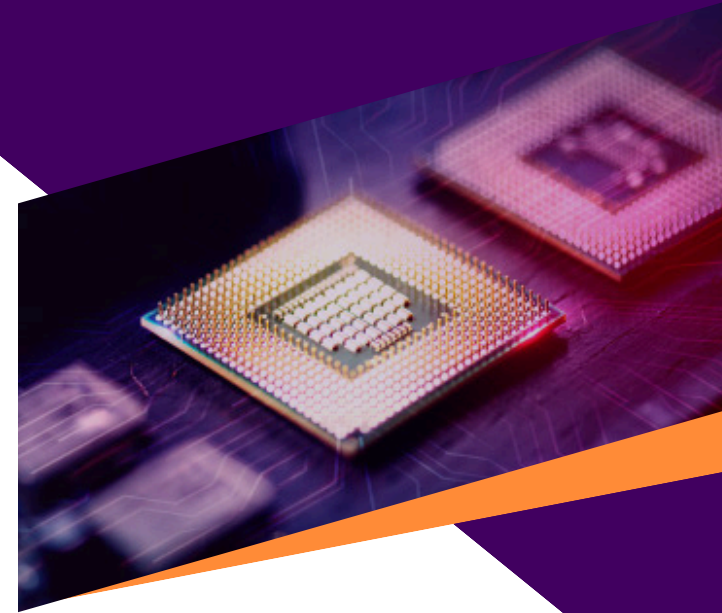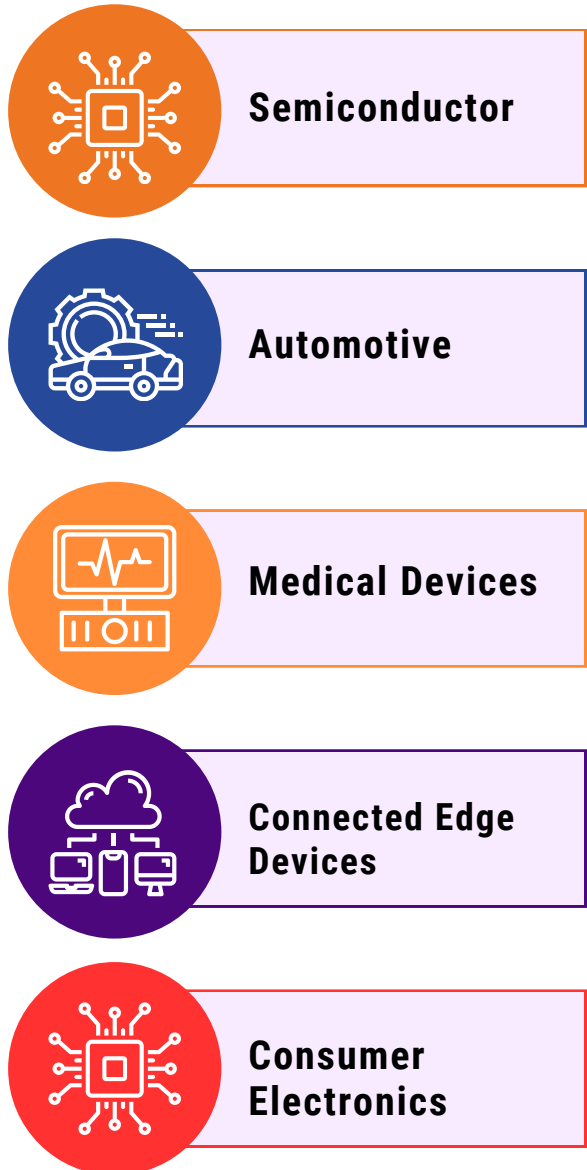| 📍 India | ▬ | Bangalore(Headquater), Hyderabad |
📍 USA	▬	San Jose
📍 Canada	▬	Markham
📍 China	▬	Beijing
📍 Malaysia	▬	Penang

SignOff ®
semiconductors

Engineering the Change
for a Device-Driven Future.

Services

1 **Digital Backend Design**
- Physical Design
- STA and Synthesis

2 **Digital Frontend Design**
- RTL Design & Integration
- DFT
- Circuit and Layout Design

3 **Circuit and Layout Design**
- Standard Cell
- Analog & Mixed Signal
- Memory
- IO PAD Library
- Design & Simulation
- AMS Verification
- Block level, IP level and Chip level Layout
- Post Layout Extraction (PEX) Simulation & Verification
- Physical Verifications

4 **Embedded Services**
-
- FPGA applications
- Custom ASIC's for various applications
- Portable / handheld devices
- Power supply / battery
- Antenna & RF frontend
- Networking
- EMC / EMI

5 **IoT Services**
- IoT Systems
- Sensor Components
- Customized ASICs for Sensor Hubs
- Turnkey IoT ASIC solutions

Domains

Semiconductor

Automotive

Medical Devices

Connected Edge Devices

Consumer Electronics

Solutions

O1 Anvay – Design Flow & Methodology

An Inter-connected and Integrated
Templated Based Design Flow & Methodology

Combining our decades of experience we ensure high performance and high quality implementation methodology that are developed and brought to production for you. We keep on looking for ways to improve, making sure the implementation methodology and its automation will stay among the best of the industry standards.

Anvay Flow is tested on TSMC 45nm/65nm & our inhouse solution "Kaveri".

O2 TurnKey ASIC Solutions

A trusted partner in
developing A-Z of a competitive ASIC

SignOff started its journey in 2015 as a Design services company and has
the experience of taping out 100+ ASIC's (cumulative as the team's total experience) that are used in various Automotive, mobile, IoT and communication based SoC applications.